



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Hayashida et al.

Serial No.: 09/939,870

Filed: August 27, 2001

Title: Semiconductor Integrated Circuit
Device and Method for Designing the
Same

Attorney Docket No.: N26180400W

Group Art Unit: 2836

Examiner: Nguyen, D.

#10
D.M. Miller
10-29-03
Response

RESPONSE TO FINAL OFFICE ACTION

5 Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

10 A. Introductory Comments

The following is submitted in response to the FINAL Office Action dated August 22, 2003. The two month Advisory Action period ends October 22, 2003.

RECEIVED
OCT 28 2003
TECHNOLOGY CENTER 2800

37 C.F.R. §1.8

I hereby certify that this correspondence is being

30 [] transmitted via facsimile to the United States Patent and Trademark Office to fax number: _____
Date of Transmittal: _____

☒ deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents,
P.O. Box 1450, Alexandria VA 22313-1450.

35 Date of Deposit: October 21, 2003

Typed/Printed Name: Darryl Walker

40 Signature: Darryl Walker